Dwyer 5-13

## IN THE UNITED STATES AND TRADEMARK OFFICE

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## **Patent Application**

Applicants(s): Dwyer et al.

Case:

5-13

Serial No.:

09/975,764

Filing Date:

October 9, 2001

Group:

2185

Examiner:

Unassigned

Title:

Method and Apparatus for Adaptive Cache Frame Locking and Unlocking

INFORMATION DISCLOSURE STATEMENT

**Assistant Commissioner of Patents** Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying PTO Form 1449. A copy of the listed item is enclosed.

- 1. Kaxiras et al, "Cache Decay: Exploiting Generational Behavior to Reduce Cache Leakage Power," IEEE, (2001).
- 2. Lai et al., "Dead-Block Prediction & Dead-Block Correlating Prefetchers," IEEE, (2001).
- 3. Mendelson et al, "Modeling Live and Dead Lines in Cache Memory System," IEEE, Trans. on Computers, v. 42, no. 1, (January 1993).

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,

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